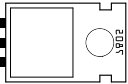
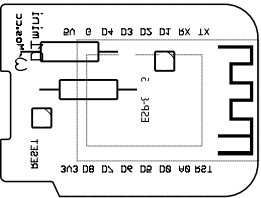




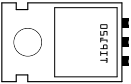
A+ 2IG END END +ACC



V2 \ V5L



T1B \ DIR



T1B750

